

The 17th IEEE International Symposium on Parallel and Distributed Processing with Applications (ISPA 2019)



***** IEEE ISPA-2019 CFP *****

The 17th IEEE International Symposium on Parallel and Distributed Processing with Applications (IEEE ISPA-2019)

<http://ispa2019.com/>

Xiamen, China, 16-18 December 2019

Sponsored by

IEEE, IEEE Computer Society, and IEEE Technical Committee of Scalable Computing (TCSC)

INTRODUCTION

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The IEEE ISPA 2019(17th IEEE International Symposium on Parallel and Distributed Processing with Applications) is a forum for presenting leading work on parallel and distributed computing and networking, including architecture, compilers, runtime systems, applications, reliability, security, parallel programming models and much more. During the symposium, scientists and engineers in both academia and industry are invited to present their work on concurrent and parallel systems (multicore, multithreaded, heterogeneous, clustered systems, distributed systems, grids, clouds, and large scale machines).

The 17th IEEE ISPA follows the tradition of previous successful IEEE ISPA conferences in the years from 2003 to 2018 in Asia, Europe, Australia and North America. It will feature sessions of regular presentations, workshops, tutorials and keynote speeches. IEEE ISPA 2019 is particularly interested in research addressing heterogeneous computing with the use of accelerators, mobile computing, approximate computing, tools and methodologies to improve the quality of parallel programming and applying generic computing approaches to networks, in particular Software Defined networking and its applications.

SCOPE AND TOPICS

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Topics of interest include, but are not limited to:

Track 1: Systems and Architectures Track

- Cloud computing and data center technology
- Migration of computations
- Multi-clouds environments, cloud federation, interoperability
- Energy management and Green Computing
- Wireless and mobile networks
- Internet-Of-Things (IoT)
- Social Networks, crowdsourcing, and P2P systems

Track 2: Technologies and Tools Track

- Building block processors: FPGA, multicore, GPU, NoC, SoC
- Parallel and distributed algorithms
- Tools/environments for parallel/distributed software development
- Novel parallel programming paradigms
- Code generation and optimization
- Compilers for parallel computers
- Middleware and tools
- Scheduling and resource management
- Performance simulations, measurement, and evaluations
- Reliability, fault tolerance, dependability, and security

Track 3: Applications Track

- High-performance scientific and engineering computing
- Grid and cluster computing
- Pervasive and ubiquitous computing
- Databases, data mining, and data management
- Big data and business analytics
- Scientific cloud systems and services
- Internet computing and web services
- Application scenarios of IoT and ubiquitous computing
- Experience with computational, workflow and data-intensive applications
- Software Defined Networks and its applications

JOURNAL SPECIAL ISSUES

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Distinguished papers selected from the conferences and associated workshops, after further extensions, will be recommended for submission and publication in the following prestigious journals or their Special Issues:

1. Knowledge-Based Systems (Elsevier): <https://www.journals.elsevier.com/knowledge-based-systems>

2. Concurrency and Computation: Practice and Experience (CCPE) : <http://www.cc-pe.net/>

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IMPORTANT DATES

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- Workshop Proposal Due: 15 June 2019
- Paper Submission Deadline: 23 September 2019
- Authors Notification: 22 October 2019
- Camera-Ready Paper Due: 17 November 2019
- Early Registration Due: 17 November 2019
- Conference Date: 16-18 December 2019

PAPER SUBMISSION GUIDELINE

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All papers need to be submitted electronically through the conference submission website (<https://easychair.org/conferences/?conf=ieeepispa2019>) with PDF format. The materials presented in the papers should not be published or under submission elsewhere. Each paper is limited to 8 pages (or 10 pages with over length charge) including figures and references using IEEE Computer Society Proceedings Manuscripts style (two columns, single-spaced, 10 fonts). You can confirm the IEEE Computer Society Proceedings Author Guidelines at the following web page:

<http://www.computer.org/web/cs-cps/>

Manuscript Templates for Conference Proceedings can be found at:

https://www.ieee.org/conferences_events/conferences/publishing/templates.html

The ISPA is ranked Class "C" by China Computer Federation (CCF). Once accepted, the paper will be included into the IEEE conference proceedings published by IEEE Computer Society Press (indexed by EI). At least one of the authors of any accepted paper is requested to register the paper at the conference.